

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	19	(capacit\$4 near2 load\$3) and (edge near2 delay) and control and (pulse near2 width) and amplif\$4 and (switch\$3 or diode).clm.	US-PGPUB	OR	ON	2005/10/15 16:54
S1	207	(369/59.12).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/14 19:06
S2	38489	(driv\$3 with circuit\$3) and (ris\$3 near3 (edge or pulse or signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:07
S3	19320	S2 and (fall\$3 near3 (edge or pulse or signal))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:07
S4	9622	S3 and capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:08
S5	8273	S4 and (switch or diode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:08
S6	6337	S5 and (resistor or (resist\$4 near2 element))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:09
S7	4332	S6 and delay	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/14 19:09
S8	614	S6 and (edge near3 delay)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/15 12:26

S9	444	(345/212).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/15 12:51
S10	47	(345/109).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/15 12:51
S11	232	(332/109).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/10/15 14:48
S12	1	capacitve near2 load near2 driving near2 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/15 14:49
S13	3	(capacit\$4 near2 load\$3) and (edge near2 delay) and (amplifier or amplify\$3) and ((switch near2 (element or device)) or diode).clm.	US-PGPUB	OR	ON	2005/10/15 16:52

FAST - [10759072A.wsp:1]

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DBs US-PGPUB

Default operator: OR

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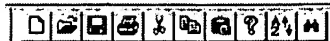
L1: (19) (capacit\$4 near2 load\$3) and (edge near2 delay) and control and (pulse near2 width) and amplif\$4 and (switch\$3 or diode).clm.

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S1: (207) (369/59.12).CCLS.

BRS form IS&R form Image Text HTML

	U	Document I	Issue Dat	Page	Title	Current OR	Current XR	Retrieval
7	<input type="checkbox"/>	US 20041024645	20041209	18	High speed comparator with blocking switches for	341/162	341/172	
8	<input type="checkbox"/>	US 20041024645	20041209	20	Capacitor calibration in SAR converter	341/120	341/172	
9	<input type="checkbox"/>	US 20041020455	20041014	38	Capacitive load driving circuit driving capacitive load	345/60		
10	<input type="checkbox"/>	US 20041017422	20040909	65	Shift register and image display device	345/102		
11	<input type="checkbox"/>	US 20041012564	20040715	24	Pulse clock/signal delay apparatus and method	327/277		
12	<input type="checkbox"/>	US 20031018762	20031023	42	System on chip for digital control of electronic device	341/141		
13	<input type="checkbox"/>	US 20031012227	20030703	37	Semiconductor device realized by using portable	365/63	257/E27.097	
14	<input type="checkbox"/>	US 20031012252	20030703	14	Switched mode power supply device adapted for	323/288		
15	<input type="checkbox"/>	US 20021018444	20021219	22	Method and apparatus for soft-switched AC power di	363/17		
16	<input type="checkbox"/>	US 20021018054	20021205	11	Efficient pulse amplitude modulation transmit modul	332/115		


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☒ L7: (444) (345/212).CCLS.

☒ L8: (47) (345/109).CCLS.

☒ L9: (232) (332/109).CCLS.

☒ L10: (1) capacitive near2 load

☒ L11: (3) (capacit\$4 near2 load

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DBs US-PGPUB

Default operator: OR

☒ Plurals☒ Highlight all hit terms initially

(capacit\$4 near2 load\$3) and (edge near2 delay) and (amplifier or
amplify\$3) and ((switch near2 (element or device)) or diode).clm.

☒ BRS form ☒ IS&R form ☒ Image ☒ Text ☒ HTML

	U	Document I	Issue Dat	Page	Title	Current OR	Current XR	Retrieval	
1	<input type="checkbox"/>	US 2005008815 8 A1	20050428	42	Semiconductor circuit device and data processing system	323/282			H e
2	<input type="checkbox"/>	US 2004024616 1 A1	20041209	18	Open loop common mode driver for switched capacitor input to SAR	341/172			L
3	<input type="checkbox"/>	US 2004020155 2 A1	20041014	38	Capacitive load driving circuit driving capacitive loads such as pixels in plasma display panels and	345/60			O a

☒ Hits ☒ Details ☒ HTML

Ready

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